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The Motorola 68000 ("sixty-eight-thousand"; also called the m68k or Motorola 68k, "sixty-eight-kay") is a 16/32-bit CISC microprocessor, introduced in 1979 by Motorola Semiconductor Products Sector.. The design implements a 32-bit instruction set, with 32-bit registers and a 32-bit internal data bus.The address bus is 24-bits and does not use memory segmentation, which made it popular with ...

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Motorola 68000 - Wikipedia

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The CPU, and later the whole family, implemented exactly two levels of privilege. ... supervisor bit was stored in the status register, and visible to user programs. A real advantage of this system was that the supervisor level had a separate stack ... design a memory interface for a 68000 processor system. CPU addresses \$000000 - \$007FFF 15!

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MC68000 Microprocessor Hardware

The Motorola 68000 series (also known as 680x0, m68000, m68k, or 68k) is a family of 32-bit CISC microprocessors. During the 1980s and early 1990s, they were popular in personal computers and workstations and were the primary competitors of Intel's x86 microprocessors. They were most well known as the processors powering the early Apple Macintosh, the Sharp X68000, the Commodore Amiga, the ...

Motorola 68000 series - Wikipedia

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64-bit Microprocessor - INTEL CORE-2: 1.2GHz to 3GHz INTEL i7: 66GHz to 3.33GHz INTEL i5: 2.4GHz to 3.6GHz INTEL i3: 2.93GHz to 3.33GHz We do not have any 128-bit Microprocessor in work at present one among the reasons for this is that we are a long way from exhausting the 64 bit address space itself, we use it a constant rate of roughly 2 ...

Introduction of Microprocessor - GeeksforGeeks

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